

PAN211x 及合封芯片软件参数更新通知

致尊敬的磐启客户：

为了进一步优化完善产品特性，更新了 PAN211x 的软件参数配置，涉及用到高增益的软件配置需要改成低增益模式，推荐使用版本 2.2.4 及以上的 sdk。

下图右边是低增益配置寄存器值，page0 的寄存器 0x5d 配置 0xdc,0x61 配置 0x2e。

```
// PAN211_WriteReg(0x34, 0x00); /* BLEWhiteList[47:40] */
PAN211_WriteReg(0x35, 0x00); /* [5:0]BLEWhiteListOffset */
PAN211_WriteReg(0x39, 0x55); // Y@Z 倍
PAN211_WriteReg(0x43, 0x3a);
PAN211_WriteReg(0x44, 0x8c);
PAN211_WriteReg(0x55, 0xdd);
PAN211_WriteReg(0x56, 0xc9);
PAN211_WriteReg(0x57, 0xb7);
PAN211_WriteReg(0x5a, 0x10);
PAN211_WriteReg(0x5b, 0xfd);
PAN211_WriteReg(0x5c, 0xe9);
PAN211_WriteReg(0x5d, 0xd4);
PAN211_WriteReg(0x5e, 0x02);
PAN211_WriteReg(0x5f, 0x06);
PAN211_WriteReg(0x60, 0x0e);
PAN211_WriteReg(0x61, 0x3e);
PAN211_WriteReg(0x66, 0x34);
PAN211_WriteReg(0x68, 0x0d);
PAN211_WriteReg(0x6e, 0x20);
PAN211_WriteReg(0x6f, 0x10);
/* 6. 射 */
PAN211_WriteReg(0x00, 0x01);

412 // PAN211_WriteReg(0x33, 0x00); /*
413 // PAN211_WriteReg(0x34, 0x00); /*
414 PAN211_WriteReg(0x35, 0x00); /* [5:
415 PAN211_WriteReg(0x39, 0x55); // 校准
416 PAN211_WriteReg(0x43, 0x3a);
417 PAN211_WriteReg(0x44, 0x8c);
418 PAN211_WriteReg(0x55, 0xdd);
419 PAN211_WriteReg(0x56, 0xc9);
420 PAN211_WriteReg(0x57, 0xb7);
421 PAN211_WriteReg(0x5a, 0x10);
422 PAN211_WriteReg(0x5b, 0xfd);
423 PAN211_WriteReg(0x5c, 0xe9);
424 PAN211_WriteReg(0x5d, 0xdc);
425 PAN211_WriteReg(0x5e, 0x02);
426 PAN211_WriteReg(0x5f, 0x06);
427 PAN211_WriteReg(0x60, 0x0e);
428 PAN211_WriteReg(0x61, 0x2e);
429 PAN211_WriteReg(0x66, 0x34);
430 PAN211_WriteReg(0x68, 0x0d);
431 PAN211_WriteReg(0x6e, 0x20);
432 PAN211_WriteReg(0x6f, 0x10);
433
434 /* 6. 射校准 */
```

PAN211x-SDK 的下载地址：

https://docs.panchip.com/pan211xdk-doc/05_resources/index.html

详细资料请定期查看磐启论坛(bbs.panchip.com)或联系我司 FAE。